Organizing Committee

1. Prof. Seema Behera (FC, IEEE Students' Chapter, SIT, BBSR)
2. Prof. Monorama Swain (FC, ISTE Students' Chapter, SIT, BBSR)
3. Prof. Bimal Meher (FC, CSI Students' Chapter, SIT, BBSR)
4. Prof. Annapurna Mishra (Sr. Asst Prof. , SIT, BBSR)
5. Prof. Sudhanshu Mohan Biswal (Sr. Asst Prof. , SIT, BBSR)

Contact Address

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E- Mail: samal.sr1981@gmail.com

Or

Prof. Pradyumna Tripathy
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For Any Other Contact

1. Subhendu Subudhi
Mob. - 09040180280

2. Rohit Kumar Rathi
Mob. - 08093615167

3. Siba Prasad Samantaray
Mob. - 09438683138

Registration Form

To, / /2015

The Director,
Silicon Institute of Technology,
Bhubaneswar

Subject : Undertaking regarding presentation for ICBPC-2015 event.

I/We, Mr/ Mrs.

1. _____,
2. _____,
3. _____,
4. _____,
5. _____,

From college _____, have applied for the event ICBPC-2015. If ,I / We will get selected by selection panel , I / We will present the project presentation before the selection panel on 20th April 2015.

Signature of Candidate:

1. _____,
2. _____,
3. _____,
4. _____,
5. _____,

Date:_____

Signature of Principal/Director
(with seal)

Silicon
...beyond teaching

An Inter-college B.Tech Project Competition

"ICBPC-2015"

20th April 2015

Technically Sponsored by

BPUT

Organized by

Silicon Institute Of Technology

Bhubaneswar

In Association With

IEEE SIT STUDENT BRANCH

Objectives

In order to enhance the quality work in terms of research in the field of Electrical Science Engineering, and Computer Science Engineering, Silicon Institute of Technology, Bhubaneswar in association with, IEEE Students' branch, ISTE Students' branch and CSI Students' branch is organizing an Inter-College B.Tech. Project Competition and poster presentation **"ICBPC-2015"** among the colleges under BPUT University, which is **technically Sponsored by BPUT**.

The competition is open to B.Tech. students in **Electrical discipline consisting of EE, EEE, ETC and AEI, in Track-I, and Computer Science and Information Technology in Track- II.**

The competition details are available in Silicon website,
www.bput.ac.in or www.silicon.ac.in

Milestone Dates

Submission of project extract by students : 11th April 2015
Information to the participants about selection : 15th April 2015
Presentation before panel & award ceremony : 20th April 2015

Topics

[1] Electrical Sciences consisting of Power system, Power Electronic, Drives, Instrumentation, Automation, Control system.
[2] Electronic communication, Signal Processing, VLSI, Embedded Systems, Image Processing.
[3] Computer Science and Information Technology.
[4] Multidisciplinary areas involving one or more of the above.

Technical Programme

- Poster Presentation
- Power-point Presentation
- Question answer and Review

Venue

Silicon Institute of Technology, Silicon Hills, Patia, Bhubaneswar-751024

Silicon Institute of Technology (SIT) was established in the year 2001 in the temple city of Orissa, Bhubaneswar with a view to provide value-based quality education. Today, the Institute is recognized as one of the centers of academic excellence and vibrant research culture in the state and outside. Over the years, Silicon Institute of Technology (SIT) has been recognized as one of the premier technological institute both in academics and research in the state of Orissa & India. The campus is about 13 kms away from the Railway Station and 16 kms away from Biju Patnaik Airport, Bhubaneswar.

Registration Fees

There will be no registration charges.

Registration Information

Participants can register by sending the completely filled-up registration form which is available in www.silicon.ac.in website along with the project details such as,

The project can be a hardware or software project. The project write up shall not exceed six typed pages and shall be on the following heads:

1. Title
2. Abstract
3. Description/ Methodology/circuit
4. Results
5. Conclusion
6. Reference

to **icbpc2015@gmail.com** . The Mail should reach us by **11th April 2015**. The participant who are shortlisted need to present a poster and Power point for the same, which shall be explained by them to the jury on the competition day (20th April 2015).

There will be 3 prizes for each track:

1st Prize - Rs.4000/-
2nd Prize - Rs.3000/-
3rd Prize - Rs.2000/-

(There will be a few consolation prizes)

Accommodation & Travel

Outstation participants shall be reimbursed to and fro 2nd class train fair or bus fare on production of bill/ticket. Participants intending to stay overnight must send prior request for accommodation (if required) on first - come - first serve basis.

Chief Patron

Prof. Dr. R.K. Patnaik
Director,SIT, Bhubaneswar

Chief-Coordinator

Prof. Soumya Ranjan Samal
Asst Prof.(ECE),SIT,Bhubaneswar

Co-Coordinator

Prof. Pradyumna Tripathy
Sr. Asst Prof.(CSE),SIT,Bhubaneswar

Advisory Committee

Prof. A.K.Tripathy,Advisor(Research), SIT,BBSR
Prof. Dr. Ajit Panda, NIST, BERHAMPUR
Prof. Dr. B. B. Mishra,Dean(Research), SIT,BBSR
Prof. Dr. Jaideep Talukdar, SIT,BBSR
Prof. Dr. N. K. Badapanda, GIET, GUNUPUR
Prof. Dr. P. K. Satpathy, CET, BBSR
Prof. Dr. S. K. Misra,Dean(Academics), SIT,BBSR
Prof. Dr. S. Pattnaik, APEX, BBSR
Prof. Dr. Srinivas Sethi, IGIT, SARANG