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Designation : Assistant Professor

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(JOINED THE INSTITUTE IN 2015)

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RESEARCH INTERESTS

- Semiconductor Device Modeling and Simulation.
- VLSI Design.

Academic Qualifications

M. E. (E.C.E) BIT, Mesra, India.

B.Tech(AEI) SIT,Bhubaneswar, India.

Teaching Experience/Industrial Experience/Research Experience

- ✓ Teaching experience-8 years
- ✓ Research experience-1 year

PUBLICATIONS

JOURNAL & CONFERENCES

- [1]. Sanjit Kumar Swain, **Satish Kumar Das** and Sarosij Adak, "Study of Linearity Performance of Graded Channel Gate Stacks Double Gate MOSFET with Respect to High-K Oxide Thickness" , Silicon, Springer, pp.1-8, August, 2019, DOI: 10.1007/s12633-019-00257-8.

- [2]. Sanjit Kumar Swain, Sudhansu Mohan Biswal, **Satish Kumar Das**, Sarosij Adak and Biswajit Baral, , "Performance Comparison of InAs Based DG-MOSFET with Respect to SiO₂ and Gate Stack Configuration" ,Nanoscience & Nanotechnology, Asia, Bentham Science, Oct, 2019, DOI: 10.2174/2210681209666190919094434
- [3]. **Satish K Das**, Sanjit K Swain, Sudhansu M Biswal, Debasish Nayak,Umakanta Nanda, Biswajit Baral, Dhananjaya Tripathy. " IEEE Devices for Integrated Circuit (DevIC 2019), pp. 365-369, DOI: 10.1109/DEVIC.2019.878327
- [4]. **Satish Kumar Das**, Sudhansu Mohan Biswal, Sanjit Kumar Swain, and Biswajit Baral, "Analysis of Junction-Less Triple-MaterialCylindrical Surrounding Gate MOSFET" International Conference on Intelligent Computing and Communication Technologies (ICICCT-2019), Springer pp. 803-812, DOI: 10.1007/978-981-13-8461-5_92
- [5]. Sanjit K Swain, **Satish K Das**, Sudhansu M Biswal, Debasish Nayak,Umakanta Nanda, Biswajit Baral, Dhananjaya Tripathy. " IEEE Devices for Integrated Circuit (DevIC 2019), pp. 510-514, DOI: 10.1109/DEVIC.2019.8783740
- [6]. Biswajit Baral, Sudhansu Mohan Biswal, Sanjit Swain, **Satish Kumar Das**, Debasish Nayak, Dhananjaya Tripathy, " IEEE Devices for Integrated Circuit (DevIC 2019), pp. 505-509, DOI: 10.1109/DEVIC.2019.8783341
- [7]. Sudhansu Mohan Biswal, Sanjit Kumar Swain, Biswajit Baral, Debasish Nayak, Umakanta Nanda, **Satish Kumar Das**, Dhananja Tripthy, " IEEE Devices for Integrated Circuit (DevIC 2019), pp. 493-496, DOI: 10.1109/DEVIC.2019.8783813
- [8]. Dhananjaya Tripathy, Debasish Nayak, Sudhansu Mohan Biswal, Sanjit Kumar Swain, Biswajit Baral, **Satish Kumar Das**, , " IEEE Devices for Integrated Circuit (DevIC 2019), pp. 310 - 313,DOI: 10.1109/DEVIC.2019.8783936
- [9]. Debasish Nayak, Umakanta Nanda, Prakash Kumar Rout, Sudhansu Mohan Biswal, Dhananjaya Tripthy, Sanjit Kumar Swain, Biswajit Baral, **Satish Kumar Das**, , " IEEE Devices for Integrated Circuit (DevIC 2019), pp. 314-317, DOI: 10.1109/DEVIC.2019.8783644
- [10]. Nifin K Mucheli, Umakanta Nanda, D Nayak, P K Rout, Sanjit Kumar Swain, **Satish Kumar Das**, Sudhansu Mohan Biswal, " IEEE Devices for Integrated Circuit (DevIC 2019), pp. 302-305, DOI: 10.1109/DEVIC.2019.8783395