



# Jagadish Kumar Patra, M.Tech.

**Designation** : Assistant Professor

**Department** : Department of Electrical and Electronics

(JOINED THE INSTITUTE IN 2018)

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## RESEARCH INTERESTS

- ✓ DC-DC Converters.
- ✓ DC Microgrid.

## Academic Qualifications

M. Tech. (Power Electronics) VIT, India (Gold)

Specialization: Power electronics.

## Teaching Experience/Industrial Experience/Research Experience

- ✓ 2 Years - CMRIT, Bangalore

## PUBLICATIONS

### JOURNAL & CONFERENCES

- [1]. "application of bio-inspired mppt techniques for Photovoltaic system",  
[http://link.springer.com/chapter/10.1007/978-981-10-1540-3\\_36](http://link.springer.com/chapter/10.1007/978-981-10-1540-3_36).
- [2]. "Four level boost converter for linear loads",  
[http://link.springer.com/chapter/10.1007/978-981-10-1540-3\\_39](http://link.springer.com/chapter/10.1007/978-981-10-1540-3_39).
- [3]. " Parameter optimization of three phase boost inverter using genetic algorithm for linear loads ",  
<http://www.sciencedirect.com/science/article/pii/S1876610216314345>.

## ANY OTHER

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Book Chapter  
Conferences attended

1. **ICAER** 2015
2. **ICRSME** 2016
3. **ICEMS** 2016
4. **ICCPEIC** 2016