



Tapas Kumar Maji, Ph.D.

Designation: Asst. Professor

Department: Department of Electrical & Electronics Engineering
(JOINED THE INSTITUTE IN 2017)

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RESEARCH INTERESTS

- ✓ Power System Analysis
- ✓ Smart Grid
- ✓ Phasor Measurement Unit (PMU)
- ✓ Soft-Computing Techniques.

Academic Qualifications

Ph. D. in Electrical Engineering, National Institute of Technology Durgapur, India
M.Tech in Electrical Engineering, West Bengal University of Technology, India
Specialization: Power System

Teaching Experience/Industrial Experience/Research Experience

- ✓ Teaching experience 02 yrs. 10 months
- ✓ Research Experience: 06 yrs.

PUBLICATIONS

SPONSORED PROJECT

- [1]. Co-PI of a Sponsored Project funded by CSIR- HRDG of an amount of 17 Lakhs (approx.).

JOURNALS

- [1]. **T. K. Maji** and P. Acharjee, "Multiple Solutions of Optimal PMU Placement Using Exponential Binary PSO Algorithm for Smart Grid Applications," in **IEEE Transactions on Industry Applications**, vol. 53, no. 3, pp. 2550-2559, May-June 2017.
- [2]. **T. K. Maji** and P. Acharjee, "A Priority-Based Multistage PMU Installation Approach for Direct Observability of All Network Buses," in **IEEE Systems Journal**, vol. 13, no. 1, pp.885-893, March 2019.
- [3]. **T. K. Maji** and P. Acharjee, "Operational-based Techno-economic PMU Installation Approach Using Grey Wolf Optimisation Algorithm (GWOA)", in **IET Generation, Transmission, & Distribution**, vol. 14, issue 1, pp. 70-78, January 2020.

- [4]. **T. K. Maji** and P. Acharjee, "A Stage-wise Optimal PMU Allocation using BCSA for Improving the Sensitive Bus Observability" in **Elsevier Procedia Computer Science**, vol. 143, pp. 702-711, 2018.

CONFERENCES

- [1]. **T. K. Maji** and P. Acharjee, "A Binary Shuffled Frog Leaping Algorithm (BSFLA) Based Phase-wise Optimal PMU Deployment with Bus Prioritization," 2017 **14th IEEE India Council International Conference (INDICON)**, IIT Roorkee, 2017, pp. 1-6.
- [2]. **T. K. Maji** and P. Acharjee, "A strategic multi-step PMU allocation based on direct monitoring for smart grid (SG) implementation," 2017 **IEEE Transportation Electrification Conference (ITEC-India)**, Pune, 2017, pp. 1-6.
- [3]. **T. K. Maji** and P. Acharjee, "Multiple solutions of optimal PMU placement using exponential binary PSO algorithm," 2015 **Annual IEEE India Conference (INDICON)**, New Delhi, 2015, pp. 1-6.

ANY OTHER

ACKNOWLEDGEMENTS

- [1]. Member of IEEE.
- [2]. Acknowledged by the Department of Electrical Engineering, NIT Durgapur for providing technical support for developing a course entitled as "Advanced power systems" under the National Mission Project on 'Education through ICT-Developing suitable pedagogical methods for various classes, intellectual calibers and research in e-learning' at NIT Durgapur on March 22, 2018.
- [3]. Received the best paper award in the track 'Smart Grid & FACTS' at the 12th IEEE INDICON Conference during December 17-20, 2015 at Jamia Millia Islamia, New Delhi, India.
- [4]. Attended more than 30 lecture courses/workshops/STTPs/FDPs/Conferences/GIAN courses.