



## Mr. Pradipta Kumar Jena, M-Tech.

**Designation:** Sr. Asst. Professor

**Department:** Department of Electronics & Communication Engg.  
(JOINED THE INSTITUTE IN 2016)

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### RESEARCH INTERESTS

- ✓ Semiconductor device Modeling and Simulation

### Or As Paragraph

### Academic Qualifications

- ✓ Ph.D. Continuing (Semiconductor device modeling and Simulation), Silicon University, Bhubaneswar, India.
- ✓ M. Tech (Electronics and Communication), Madras Institute of Technology, Anna University, Chennai, Tamilnadu, India.

### Teaching Experience/Industrial Experience/Research Experience

- ✓ Worked as Lecturer in Rajiv Gandhi College of Engineering, Sriperumbudur, Chennai from 2005 to 2012.
- ✓ Worked as Asst Professor in Templecity Institute of Technology and Engineering, Barunei, Khurda from 2012 to 2014.
- ✓ Worked as Asst Professor in Gandhi Institute for Technology, Pitapalli, Khurda from 2014 to 2016.
- ✓ Working as Sr. Asst. Professor in Silicon University, Silicon University, Odisha from 2016 till date.

### PUBLICATIONS

### JOURNAL ARTICLES & CONFERENCE PAPERS

- [1]. Pradipta Kumar Jena, Sanjit Kumar Swain, Omprakash Acharya, Sarosij Adak, "Study of Linearity Performances of Junction-less Triple-Material Cylindrical Surrounding Gate MOSFET." "2018 International Conference on Applied Electromagnetics, Signal Processing and Communication (AESPC)".

- [2]. Pradipta Kumar Jena, Prasanta Kumar Khuntia, Biswajit Baral, Sudhansu Kumar Pati, "Impact of Gate Engineering on Analog, RF Performance of Nanoscale Barriered TM-Heterostructure DG-MOSFET," 2022 IEEE Electron Device Kolkata Conference.
- [3]. P. K. Jena, S. Das, S. Bardhan, S. Misra, B. Baral, and S. K. Pati, "Low Frequency Noise Analysis in AlGaIn/GaN HEMTs," 2023 1st International Conference on Circuits, Power, and Intelligent Systems (CCPIS), Bhubaneswar, India, 2023, pp. 1-4, doi : 10.1109/CCPIS59145.2023.10291825.
- [4]. S. Bardhan, P. K. Jena, S. Misra, S. Das, B. Baral, and S. K. Pati, "Analytical Volume Inversion Charge density Modeling of SGCG DG MOSFET in the presence of Interfacial Traps," 2023 1st International Conference on Circuits, Power, and Intelligent Systems (CCPIS), Bhubaneswar, India, 2023, pp. 1-6, doi : 10.1109/CCPIS59145.2023.10291376.

## ANY OTHER

### Book Chapter Conferences attended

- [1]. AICTE Training And Learning (ATAL) Academy Online Advanced FDP on " IOT in 5G Technology" from 2021-06-21 to 2021-06-25 at Silicon University, Bhubaneswar, India .
- [2]. 1st International Conference on Circuits, Power, and Intelligent Systems (CCPIS), Silicon University, Bhubaneswar, India, 2023.
- [3]. Attended Online Webinar on Industry Internet of Things-2020 organized by department of Electronics Engineering at Silicon University from 11-13<sup>th</sup> November 2020.
- [4]. Attended Online Webinar on Nano-Technology and Embedded Systems NES-2020 organized by department of Electronics Engineering at Silicon University from 15-17<sup>th</sup> December 2020.
- [5]. Attended Winter Webinar on Signal Processing(WWSP-2021) organized by department of Electronics Engineering at Silicon University from 18-22nd January 2021.