Silicon University

SiliconTech

A QUARTERLY NEWSLETTER

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EDITORIAL

Greetings from Silicon!

This is a moment of joy and happiness when I am writing this editorial for our newsletter when Silicon is entering its 25th year. Although 25 years may not be a very long time for an organization but for the people who have started with it and lived with it during its formative years, it means a lot.

Celebrating a "Silver Jubilee" year is always a moment of happiness and fulfillment. I am happy and proud to be a part of such a celebration of Silicon. I am also fortunate to be one among few employees of Silicon who were there during its formation year and are still continuing at the time of the Silver Jubilee celebration. With growth and achievement there are challenges. In the past, Silicon has met many challenges and overcome those due to dedication and hard works of its students and employees and co-operation from all stakeholders. This is a moment when all stakeholders, old and young, together, look back at the 25 years of successful journey that Silicon has gone through, also foresee the next twenty-five years of development. This is a moment of fulfillment without being complacent.

Starting with a humble beginning with four UG programs, Silicon has reached a level with several PG programs ranging from Data Science, VLSI to Molecular Medicine. This year we have welcomed the first batch of students for B. Tech. in Electronics Engineering (VLSI Design & Technology) at an appropriate time when the Government of Odisha with able support from the Government of India is taking a major step into semiconductor technology to lead India to an era of Self Sustainment. Also with more than hundred students in Ph.D. programs, Silicon is not far behind the frontier of research and innovation. After becoming a University in January 2024, Silicon is now in a position with appropriate strategy to develop its research infrastructure further to meet the global challenges and contribute to the technical growth of the nation.

Silicon will see further growth, reach new milestones and will make all of us proud again and again. During this yearlong celebration of Silver Jubilee, I congratulate all students, alumni, employees and stake holders of Silicon - wish you all good health, happiness and prosperity.

Dr. Saroj Kanta Misra
Advisor, Silicon University

UNIVERSITY HIGHLIGHTS

NIRF Rankings 2025 recognizes Silicon University among India's top 300 engineering universities and colleges

Silicon University has been ranked in the 201–300 band in the Engineering category of the NIRF Rankings 2025, announced by Hon'ble Minister of Education Shri Dharmendra Pradhan on 4 September 2025. Out of 1,584 participating engineering institutes across India, only the top 300 were ranked. Silicon is one of twelve institutes from Odisha to be ranked within the top 300 in NIRF Rankings 2025.

Silicon University hosts its second Convocation

Silicon University hosted its 2nd Convocation on 2 August 2025, graced by Hon'ble Governor of Odisha and Visitor, Dr. Hari Babu Kambhampati. Chief Guest Dr. Anand Deshpande, Founder and MD of Persistent Systems, delivered the convocation address, while Prof. Peter Constantine, Director of the Literary Translation Program at the University of Connecticut, attended as Guest of Eminence. Degrees were conferred upon 672 students, and awards including the Best Graduate Award, Best Student Award, gold medals, and merit certificates were presented.

CCPIS 2025

Silicon University, Bhubaneswar, hosted the 2nd International Conference on Circuits, Power, and Intelligent Systems (CCPIS 2025) from 5–7 September 2025 in a hybrid format. Technically co-sponsored by IEEE and its affiliated chapters, the event featured global researchers, industry leaders, and academicians. Highlights included keynotes by experts from Microsoft AI, IIT Madras, and Astera Labs. With 667 submissions and 131 presentations across six tracks, the conference fostered innovation and collaboration. Special sessions by IEEE Young Professionals and WiE added value. CCPIS 2025 concluded with the presentation of best paper awards.

Silver Jubilee

SiliconTech celebrated its Silver Jubilee on September 10, 2025, marking 25 years of excellence in education, research, and innovation under the theme "Silver Legacy, Golden Future." The event featured the unveiling of the Silver Jubilee logo, a special film highlighting the institution's journey, and inspiring speeches by Vice Chancellor Dr. Jaideep Talukdar, Chief Guest Prof. Shreepad Karmakar, and other dignitaries. Cultural performances blending tradition and innovation enriched the celebration. With alumni, faculty, and students coming together, the event reaffirmed SiliconTech's legacy and commitment to shaping a bright future through academic excellence and community spirit.

NEWS AND EVENTS

A seminar on 'Sharpen Your Edge – Academics, Attitude and Ability'

The Industry Interface Cell (II Cell) at SiliconTech organized a seminar titled 'Sharpen Your Edge – Academics, Attitude and Ability' on 29 August 2025 to guide final-year students in strengthening academic foundations, developing a professional attitude, and building industry-ready skills. Mr. G. D. Sharma, Independent Director and former President–HR at BGR Energy Systems Ltd., delivered an engaging session drawing from his leadership experience at L\&T and Vedanta. The seminar concluded with a vote of thanks, leaving attendees motivated and better prepared for their careers.

AICTE-VAANI sponsored National Workshop

The Department of Electrical and Electronics Engineering (EEE) at SiliconTech organized a three-day AICTE-VAANI sponsored National Workshop on 'Energy Efficiency and Sustainable Development in the Era of Climate Change' from 31 July to 2 August 2025. Aimed at promoting technical discourse in Odia under AICTE-VAANI and ATAL Academy, the workshop featured expert sessions on renewable energy, smart grids, and energy audits. Distinguished speakers from academia and industry shared their insights in Odia.

FDP on Participative Teaching-Learning

SiliconTech organized a two-day Faculty Development Program (FDP) titled 'Teaching to Engage: Transitioning from Lectures to Participative Learning' on 16 & 17 July 2025, aiming to equip faculty with interactive, learner-centric teaching strategies. Facilitated by Ms. Sraboni Sengupta and Mr. Rajan Kumar Majumdar from Constellation Training & Placement Services, the sessions introduced methods like Think-Pair-Share, Case Method, and digital tools such as Mentimeter and Padlet. Forty faculty members participated in the FDP to redesign lessons and practice microteaching with peer feedback.

Workshop on Deeptech in Climate Action

The Eco-Social Club organized a one-day workshop titled 'DeepTech in Climate Action – Use Cases and Future' on 20 September 2025, aimed at exploring how advanced technologies like AI, IoT, biotechnology, and quantum computing drive climate resilience. Inaugurated by Prof. Subrat Kumar Sahu and Mr. Debadarshan Mohanty, CEO of GoCarbonPositive, the event featured sessions on practical applications such as renewable energy, carbon capture, and sustainable agriculture, alongside future prospects in building a low-carbon economy.

Invited talk on Translation of Greek and Indian Scientific Texts

The Department of Basic Sciences and Humanities (BSH) at SiliconTech organized an invited talk titled 'Bridging Civilizations: The Translation of Greek and Indian Scientific Texts' on 1 August 2025. Professor Peter Constantine from the University of Connecticut, a distinguished translator and scholar, delivered a thought-provoking lecture on the historical and cultural challenges of translating classical scientific texts, highlighting language evolution and cultural nuances. Joined by Dr. Tanutrushna Panigrahi, Buxi Jgabandhu Chair Professor (Govt. of India) at Utkal University, the session also explored the role of Artificial Intelligence in translation.

Invited talk on 'Video compression: An Engineer's perspective'

SiliconTech hosted a talk by Dr. Gagan B Rath, Research Staff Member at InterDigital R&I, Rennes, France, on 'Video compression: An Engineer's Perspective' on 21 September 2025. Dr. Rath shared valuable insights into the technical challenges and innovations in video compression, highlighting its critical role in modern communication systems. His talk offered students and faculty members a deep understanding of the engineering principles behind efficient video data processing, emphasizing real-world applications and future trends. The session was highly engaging, motivating attendees to explore advanced research in multimedia technologies.

Collaboration with Vedanta-FACOR

SiliconTech has strengthened its industrial consultancy profile through a major collaboration with Vedanta-FACOR, a key player in India's ferroalloy sector. On 16 July 2025, SiliconTech received its first consultancy project from FACOR, titled 'Power System Study & Relay Coordination', focused on the Ostapal Chromite Mines in Odisha. Led by the Department of Electrical and Electronics Engineering (EEE), the project aims to enhance system reliability, safety, and performance. This engagement marks a significant milestone in SiliconTech's industry outreach, showcasing its capability to provide real-world technical solutions to complex industrial power challenges.

High-impact consultancy project for HINDALCO

SiliconTech successfully completed a high-impact consultancy project for HINDALCO at its Hirakud Smelter Plant, focusing on enhancing electrical safety systems through improved lightning arresters, earthing, and surge protection. The project, received on 12 November 2024 and concluded on 11 July 2025, was led by the EEE Department and involved a thorough assessment of existing infrastructure and the proposal of cost-effective, technically sound alternatives. Well-received by HINDALCO, the recommendations are being considered for phased implementation.

Obscura 2025

SiliconTech, in collaboration with the Ketaki Foundation Trust, hosted Obscura 2025 on 19 August 2025 to celebrate World Photography Day. The event aimed to promote photography as a powerful medium of expression, inspiring students to explore both its artistic and technical dimensions. Featuring expert talks by renowned photographers Shri D.K. Pattanayak and Shri Pramod Dhal, along with a curated student exhibition showcasing diverse themes, Obscura 2025 provided a vibrant platform for creativity and storytelling, emphasizing photography's role in capturing unique perspectives and meaningful narratives.

Independence Day 2025

SiliconTech celebrated India's 79th Independence Day on 15 August 2025 with a vibrant event honoring the nation's freedom struggle and fostering patriotism. The celebration began with our Vice-Chancellor Dr. Jaideep Talukdar hoisting the national flag, followed by a collective rendition of the national anthem. Dr. Talukdar's speech reflected on India's progress and citizens' duties in preserving freedom. The program featured traditional cultural performances, inspiring patriotic speeches, and engaging activities that celebrated India's rich heritage, uniting students, faculty, and staff in pride and solidarity.

Azad Hind

The Meta Academics Cell at SiliconTech organized the Azad Hind Event on 12 and 13 August 2025 as part of Independence Day celebrations, encouraging students to reflect on India's journey- the present and the future. The event featured essay and speech competitions designed to foster critical thinking and national values. Eleven students participated in the essay contest and eight in the speech competition, addressing themes of freedom, unity, and progress. Judges offered constructive feedback, enhancing student learning. With active participation and meaningful discussions, the event served as a heartfelt tribute to India's independence and student-driven civic expression.

'Van Mahotsav 2025'

SiliconTech's Eco-Social Club, in collaboration with the Nature and Wildlife Conservation Society of Odisha, organized a plantation drive to celebrate Van Mahotsav 2025 on 7 July 2025. Aimed at promoting afforestation and environmental awareness, the event emphasized the importance of trees in combating climate change and preserving biodiversity. Fifty participants took part in the drive, planting saplings and engaging in discussions on ecological responsibility. The initiative fostered unity and environmental stewardship, reflecting SiliconTech's commitment to sustainability and community involvement.

Blood Donation Camp

SiliconTech organized a Blood Donation Camp on 17 September 2025, marking the first event of its Silver Jubilee Celebrations. The camp aimed to promote voluntary blood donation and raise awareness about its life-saving impact, encouraging social responsibility among students, faculty members, and staff. Inaugurated by our Vice-Chancellor Dr. Jaideep Talukdar, the event was jointly organized by the Eco-Social Club and Unit-6 Capital Hospital Red Cross team. With active participation from 184 donors, the event showcased SiliconTech's commitment to community welfare and set a meaningful tone for the Silver Jubilee year.

Zero Tolerance Quiz

The Zero-Tolerance Quiz, organized by the Silicon Quiz Club on 16 August 2025 promoted awareness against ragging and bullying. Conducted by Quiz Master Deepak Kumar Gouda (3rd Year, CSE), the quiz saw enthusiastic participation from over 30 students. With a prize pool of ₹3000, the event featured a prelim round and a thrilling final. Winners included Biswanath Patnaik and Swaraj Mahapatra, with Aditya Sahoo as 1st Runner-Up, and Akankshya Panda and Pratyasha Sahoo as 2nd Runners-Up. The event highlighted SiliconTech's commitment to a safe, respectful campus culture.

Meme Quiz

The Silicon Quiz Club successfully organized the Meme Quiz on 19 September 2025. Designed around memes and current social media trends, the quiz challenged students' wit and creativity in a fun, engaging setting. Conducted by Priyanshu Das (2nd year, Electrical & Electronics Engineering), the event attracted over 90 enthusiastic participants, with a ₹3000 prize pool. The two-tier format included a preliminary round of 15 questions and finals comprising three rounds plus tie-breakers.

Induction of the 2025 admission batch

Silicon University successfully conducted a one-week induction program for the 2025 B.Tech. admission batch from 11 to 14 August 2025, following AICTE/UGC guidelines. The program aimed to induct students into university life by introducing academic structures, human values, and support services. Sessions included talks by the Vice-Chancellor, deans, faculties-in-charge of clubs, and guest speakers on academics, career planning, mental health, and student services.

Internal Hackathon for SIH

The Silicon Innovation and Promotion Cell (SIPC) hosted an internal hackathon on 19 & 20 September 2025 as part of its Silver Jubilee celebrations, inviting 64 teams to craft technology-driven solutions for tomorrow's challenges. Among the participants, 50 teams competed in the software domain, while 14 teams focused on hardware projects. The Internal Hackathon was smoothly coordinated by the university's Single Point of Contact (SPOC), who facilitated communication with the SIH portal, guided students through the idea submission process, and oversaw the efficient execution of the event.

QCFI 2025

Silicon University, in collaboration with the Quality Circle Forum of India (QCFI), Bhubaneswar Chapter, hosted the 7th Chapter Convention on Quality Concepts (CCQC-2025) on 13 & 14 September 2025. The event, themed 'Quality Concepts for Atma Nirbhar Viksit Bharat', aimed to foster knowledge exchange and promote quality circles as drivers of grassroots innovation. Inaugurated by Mr. S. S. Mohanty, President of QCFI, the convention featured over 210 teams from leading industries like NALCO, Tata Steel, and Jindal Steel presenting case studies and competing in various quality-related contests.

Grand Job Carnival 2025

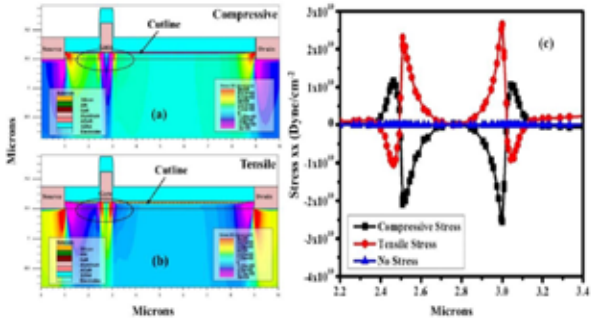
The AVD Group, collaborated with SiliconTech, to host Odisha's Grand Job Carnival 2025 on 18 & 19 September 2025. The event aimed to create a high-impact employment platform for fresh graduates and experienced professionals, facilitating direct interaction between job seekers and recruiters across diverse industries. Inaugurated by dignitaries including Dr. Jaideep Talukdar, our Vice-Chancellor and Aatish Patil, Founder of AVD Group, the carnival attracted over 4,000 candidates. More than 30 reputed companies, such as Infosys, Persistent Networks, and Hyscaler, conducted on-spot interviews for roles like Software Developer, UI/UX Designer, and Business Analyst. This grand job fair offered internships, full-time employment opportunities and valuable insights into industry trends.

Note: *For further details on any news item, visit https://silicon.ac.in/news/*

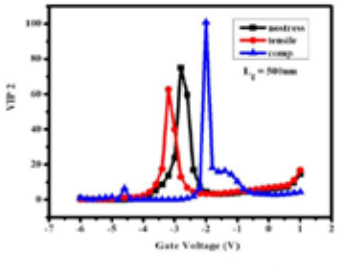
- Scopus/SCI Indexed journals: **13**
- Conference Proceedings: **1**
- Book Chapters: **1**
- Patents: **1**

Process-induced stress tuning to improve linearity performance in AlGaN/GaN HEMTs with SiNx passivation

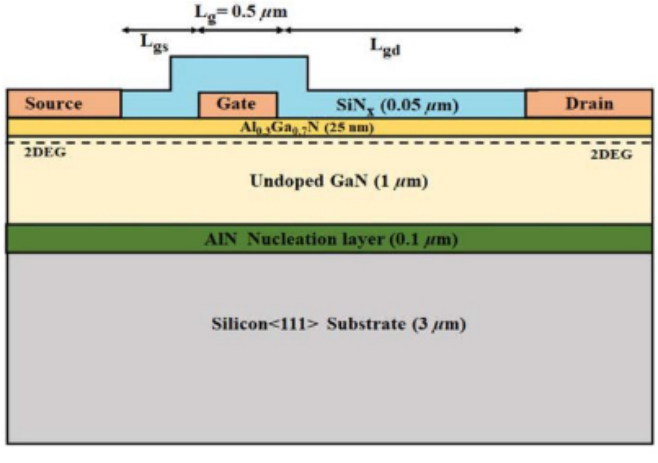
Strain engineering has proven to be a useful technique for enhancing the performance of many modern-day transistors. Stress engineering can also have a non-trivial effect on the performance of GaN High Electron Mobility Transistors (HEMTs), which are devices of choice for high-power, high-frequency microwave applications. Process-induced stress can have a significant effect on AlGaN/GaN HEMT electrical characteristics in addition to the growthinduced strain present due to lattice constant mismatch between device layers. Understanding the true impact of process-induced strain on AlGaN/GaN HEMT microwave performance is highly necessary. This is especially the case for nonlinearity effects which affect the high-frequency performance of the device. This work uses a systematic combination of TCAD process simulation and device simulation to quantify the effect of process-induced stress on the device's linearity. This work demonstrates that nonlinearity effects can be minimized through proper tuning of stress by varying design-dependent parameters such as the nitride layer thickness. The effect of stress on highly important linearity parameters of the device like gm, gm2, gm3, VIP2, VIP3, IIP3 and IMD3 is investigated in detail. Comparing the compressive-stress device to the no-stress and tensile-stress devices, we conclude that the compressive stress device has a max gm2 value that is 61% and 71% higher respectively. The max gm3 for compressive stress is 0.13 AV−3 whereas those for no-stress and tensile stress are 0.067 AV−3 and 0.045 AV−3 respectively. However, the voltage-variations of VIP2, and the VIP3 parameters which are derived from these gm values shows that the compressive stress case can help achieve overall better device linearity by stress tuning. This work also studies how the effect of process-induced stress on device linearity varies with the crucial gate length parameter. The results are shown below: Structure of the Device (a) and (b) Stress profile along the channel for compressive and Tensile stress device with gate length=0.5µm. (c) Stress xx along the x-axis for no-stress, compressive, and tensile stress devices with gate length=0.5µm. VIP2 as a function of VGS for no-stress and stress device at VDS=3 Vand LG=0.5 µm.



(a) and (b) Stress profile along the channel for compressive and Tensile stress device with gate length=0.5µm. (c) Stress xx along the x-axis for no-stress, compressive, and tensile stress devices with gate length=0.5µm.



VIP2 as a function of VGS for no-stress and stress device at VDS=3 Vand LG=0.5 µm.



Structure of the Device

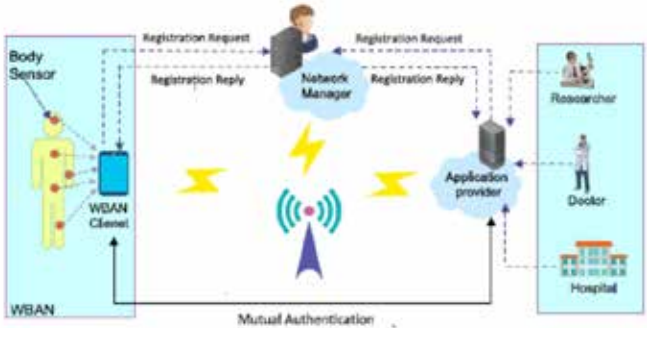
This research is being conducted by Dr. Sanghamitra Das, Assistant Professor, Electronics and Communication Engineering

Efficient Certificate-less Anonymous Mutual Authentication in WBANs for Smart Healthcare

In recent years, Wireless Body Area Networks (WBANs) have emerged as a vital enabler of smart healthcare systems, facilitating continuous monitoring and real-time diagnosis of patients through wearable or implantable sensors. A core challenge in this domain is ensuring robust mutual authentication between communicating entities (WBAN clients and Application Provider) and establishing secure key generation mechanisms without compromising computational efficiency or user privacy (Fig.1). Many existing authentication protocols fall short in fulfilling critical security requirements such as strong user anonymity, resistance to key escrow, and key confidentiality. Moreover, traditional Public Key Infrastructure (PKI)-based approaches often introduce substantial overhead due to certificate management, making them unsuitable for WBANs where devices have limited computational power and storage capacity.

In this research, we introduce a novel certificate-less anonymous mutual authentication algorithm for WBAN environments as shown in Fig.2. The proposed scheme is both computationally lightweight and memory-optimized, designed specifically to function under the constrained resources of wearable healthcare devices. It leverages a hybrid cryptographic model that combines techniques based on the Discrete Logarithm Problem (DLP), Elliptic Curve Discrete Logarithm Problem (ECDLP), and Bilinear Pairings to ensure a high level of security without incurring excessive overhead. By eliminating the reliance on certificates while ensuring identity privacy and traceability, the proposed scheme achieves strong mutual authentication and secure session key establishment. It is resilient to a wide range of attacks, including impersonation, replay, and man-in-the-middle attacks. Moreover, the scheme effectively prevents key escrow, a vulnerability in identity-based cryptography where a trusted third party can reconstruct users' private keys.

To demonstrate the effectiveness of the proposed protocol, formal and informal security analyses were conducted. The scheme was validated using formal logic frameworks such as the Real-Or-Random (ROR) model, as well as through simulations and performance evaluations. Experimental results show that, compared to the best existing approaches, the proposed scheme reduces computational cost by at least 15%, minimizes storage requirements by approximately 41%, and lowers communication overhead by 10% (Fig.3). Therefore, this work presents a balance between the security and scalability for resource-constrained systems while contributing significantly to the advancement of secure communication in next-generation healthcare systems.



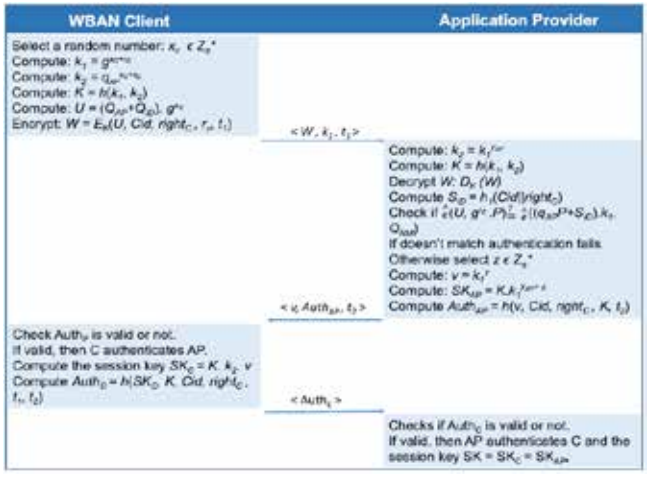


Fig.2 Mutual authentication algorithm between WBAN client and AP.

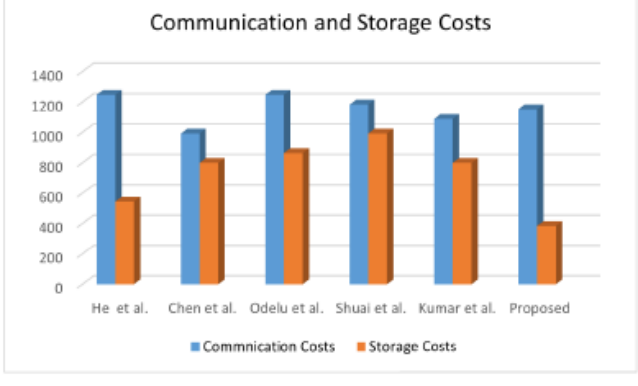


Fig.2 Mutual authentication algorithm between WBAN client and AP.

This research is being conducted by Dr. Bimal Kumar Meher, Associate Professor, Computer Science and Engineering

Yuvraj Biswal (CEN, 2027) secures an internship at Dognosis

Yuvraj Biswal from the B.Tech. Computer Engineering (CEN) batch of 2027 has secured an internship at Dognosis, a neuroscience-AI startup focused on canine behaviour analysis. He will receive a stipend of ₹1.2 lakh per month during his internship. At Dognosis, he will contribute to the development of backend Application Programming Interface (APIs) and micro services, playing a key role in building scalable systems for real-time processing of multimodal data like video, EEG, and sensor inputs.

Om Prakash Behera (CSE, 2025) gets selected in IIM Sirmaur

Om Prakash Behera, from the B.Tech. Computer Science Engineering (CSE) batch of 2025 has been selected for IIM Sirmaur. He prepared for CAT alongside final-year academics, juggling coursework, mock tests, and a rigorous study schedule while being disciplined and determined.

Practice School (PS) Selectees from 7th Semester B.Tech. graduating batch of 2026

Haber Technologies@35K

Rishu Kumar (CSE)

Lavkush Solanki (CSE)

Rahul Kumar Singh (CSE)

Radhika Exclusives @20K

Anudeep Bayard (CSE)

StratLytics @20K

Anuska Pattnaik (EEE)

Suchismita Sahoo (CSE)

Ankur Dey (CSE)

Shiv Kumar Behera (CSE)

Gabrélon Corp @15K

Anugrah Gabriel (CSE)

Gravity Engineering Services @10K

Suchismita Das (CEN)

Om Narayan Mishra (EEE)

RIAXE Systems Pvt Ltd @10K

Lokamanya Lohit Lenka (EEE)

Lex Protector LLP @8K

Swadesh Panda (CST)

ITR, DRDO @5K

Anshuman Tripathy (CSE)

CABS, DRDO

Om Prakash Panda (ECE)

SAC (ISRO)

Sakshi Prasad Singh(CSE)

Squbix Digital Pvt. Ltd.

Abed Akhtar

INDUSTRY INTERFACE CELL

PLACEMENT HIGHLIGHTS

SiliconTech's Industry Interface Cell started its campus placement drives for the 2026 graduating batch from the first week of August 2025, mostly conducted in a physical mode. Despite the current slowdown in the job market, several leading companies from both the Core and IT sectors have successfully completed their recruitment processes.

So far, more than 21 companies have visited the campus to offer placement opportunities. In the IT services and product development space, companies such as **Argusoft, Aptus Data Lab, B.I.Pros, Codemate.ai, Gravity Engineering, Gyansys, Haber Technology, Inovaare, Infosys, Rumango Technologies, StartLytics, Surya Digital, Techrays Labs** have concluded their drives.

In the VLSI and Core domains, CoreEL and Moschip have shared their results, while the recruitment process with **JSW** is currently underway.

Till date, the 2026 graduating batch has received around **255 job offers,** with salary packages ranging from **₹3.6 LPA** to **₹10 LPA**.

PRACTICE SCHOOL (PS) HIGHLIGHTS

Recognizing the increasing industry demand for long-term internships, the II Cell has introduced a policy that allows students to undertake a 1-year internship starting from the 7th semester, providing them with valuable hands-on industry exposure and practical learning experience.

- **80** students from the 2026 batch have already been selected to undergo the PS / 1-year internship program.
- **17** students from the 2026 graduating batch have been selected by different companies for doing their Practice School (PS) program in the 7th semester. The selected students have already joined their respective PS stations and they will spend one semester in their work location for the industry internship under the PS program.
- Furthermore, **63** students from the 2026 graduating batch have already been selected under the 1-year internship program to work with **13 different companies**.
- Most of the students selected for the PS program and the 1-year internship program will receive a monthly stipend in the range of **INR 10K- 50K** during their internship period.

The companies who have selected our students for the PS program or 1-year internship includes:

- Atommorph
- BIPROS
- CABS, DRDOGabrélon Corp
- Gravity
- Haber
- Lex Protector LLP
- Inofinity Research and Development
- Inovaare
- ITR DRDO
- Logile
- Naada Technologies
- Radhika Exclusives
- RIAXE SYSTEMS PVT LTD
- SAC (ISRO)
- SAP Internship
- Shelduck AI Systems
- Squbix Digital
- Stratlytics
- Surya Digital
- Viden Edu Tech
- Yukti Verse
- Zeon AI Labs

Practice School drive by Surya Digital

Practice School drive by Stratlytics

PRE-PLACEMENT TRAINING

The Pre-placement training for the 2027 graduating batch has already started from the 1st week of September 2025 in the virtual mode. The students who have enrolled in this course will be trained in aptitude, reasoning, soft skills, and programming skills. As a part of this training program, all students will be provided a dedicated LMS-based platform where they can access practice tests, assignments, and questions asked in an interview and can also apply for different digital internships.

SUMMER INTERNSHIP 2025

SiliconTech's Summer Internship Program 2025, held from 2 June to 12 July 2025, offered a diverse range of industry-oriented and skill-enhancing courses across emerging technologies. The program was designed for B.Tech., M.Tech., MCA, IMCA, and M.Sc. Data Science students of 1st, 2nd, and 3rd year, providing them with hands-on experience under expert guidance. With a blend of online and offline sessions, it ensured both flexibility and engagement, allowing participants to explore cutting-edge tools and technologies. The initiative reflected SiliconTech's commitment to fostering experiential learning, industry readiness, and innovation through expert-led sessions and mentorship from experienced faculty members. The internship also involves the participation of various external students as well.

SI Highlights 2025*

- **16** Domains
- **32** Programs in blended mode of learning
- **2392** Students benefited
- Around **13%** Students to get payback
- **62** External students participated in SI program

SI Summary - Last 5 years

Year	No. of courses offered	No. of Students Registered
2021	27	1414
2022	25	1575
2023	33	1911
2024	36	2122
2025	32	2392

* Conducted at Silicon Unicersity

I thoroughly enjoyed my Gen AI internship—it was an amazing experience exploring AI and its real-world applications. Each task taught me something new, boosting my confidence and curiosity to learn more in this exciting field.

- Jasmita Seth
B.Tech. ECE, 2027
GenAI with Keras, Pytorch & ChatGPT

I'm grateful for this course and amazing mentors, which helped me explore my career path. I look forward to more opportunities in cloud computing to enhance my skills and growth.

- Sambit Kumar Parida
B.Tech. ECE, 2027
Introduction to Azure Data Engineering

I'm thrilled to have taken this engaging course! It was an enriching experience that taught me valuable new skills and insights. I'm eager to apply what I've learned and excited to explore more courses that will help me grow.

- Swetalina Dhalbisoi
B.Tech. ECE, 2026
Full Stack with Java Spring Boot with Angular MYSQL

I learned many new things in this course, especially about Python applications and development, which will help me in the future. I am looking forward to enrolling in more courses like this to further enhance my skills.

- Asim Abhinav Das
B.Tech. EIE, 2028
Python Application and Development

ENTREPRENEURSHIP DEVELOPMENT CELL

MSME Idea Hackathon 5.0 event

Silicon MSME Incubator participated in the MSME Idea Hackathon 5.0, inviting theme-based ideas under the MSME Innovative Scheme on 16 August 2025. The incubator received 10 innovative submissions from students, innovators, and MSMEs across India. A scrutiny committee of academicians, industry experts, and entrepreneurs evaluated the ideas, inviting all participants to present their concepts. Two ideas were shortlisted: a smart portable IoT-based medical cooler by Mr. Smrutiranjan Ranjan Rout and a comprehensive hospital-at-home solution by Mr. Ganesh Behera. These ideas have been forwarded to the MSME Office for funding support up to ₹15 lakhs.

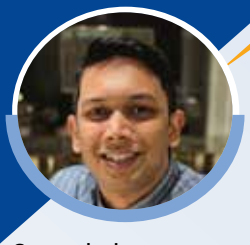
Entrepreneurs' Day 2025

Silicon University's Entrepreneurship Development Cell and Institution's Innovation Council celebrated Entrepreneurs' Day 2025 on 21 August 2025 to inspire innovation and leadership among students. 150 first-year students, faculty members, and senior student leaders participated in the event. Prof. Dr. Mahendra Prasad Agasty highlighted entrepreneurship's role in nation-building and encouraged students to utilize university resources like the ED Cell and Incubation Centre. Other faculty members shared insights on nurturing ideas and bridging academics with real ventures. An interactive entrepreneurship quiz engaged attendees, with winners receiving prizes, making the event both educational and enjoyable.

ALUMNI BUZZ

The Department of Computer Science & Engineering (CSE) conducted the 'AI for Beginners' program over four Saturdays starting from 30 August 2025, introducing 25 B.Tech. CSE students, faculty members, and staff to AI fundamentals and recent advancements. Led by alumnus Mr. Akash Dash, a Data & AI Engineer at Accenture, the program covered practical sessions on Large Language Models, prompt engineering, and AI tools like ChatGPT and Google AI Studio. It concluded with discussions on AI ethics, automation, and project development, culminating in a showcase of AI-assisted applications developed by participants.

ALUMNI IN FOCUS



SARANSH MOHAPATRA
Senior Consultant at Ernst & Young (EY)
B.Tech. (EEE) 2010-2014

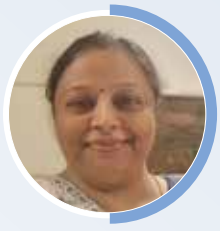
Saransh has spent over 11 years building a diverse career across the Wealth & Asset Management, Healthcare, and Manufacturing sectors. Currently a Senior Consultant at Ernst & Young (EY), he leads end-to-end product implementations and manages stakeholders across large-scale programs. Beyond the workplace, he is an accomplished quizzer with multiple wins in open and corporate quizzing circuits. He's also an active quizmaster, regularly hosting events for college, corporate, and public audiences. A sports enthusiast, he's often found on badminton and squash courts, competing in both corporate and open tournaments. Whether it's at work or outside of it, Saransh enjoys staying engaged, learning new things, and being part of vibrant communities.

JYOTI PRABHAKAR
Deputy Superintendent of Police in the Anti-Corruption Branch (ACB), CBI Mumbai
B.Tech. (E&T) 2008-2012

Jyoti was selected by Infosys and Capgemini during the campus placements, but later chose to pursue his aspiration of serving the nation in public service. Currently he is serving as Deputy Superintendent of Police in the Anti-Corruption Branch (ACB), CBI Mumbai. Over the course of his service, he has investigated several anti-corruption cases including JEE paper leak incidents and disproportionate assets cases contributing towards transparency and accountability in public service. His growth trajectory reflects a balance of technical innovation and athletic spirit — he was awarded First Prize in Automatic Robotics, and during his CBI training, he was adjudged both the Best Sportsperson and Best Athlete. Currently he is also enrolled in LLB course in Mumbai University.

SHASHANK SHEKHAR
Senior Software Engineer, Gyansys Infotech
B.Tech. (EEE) 2016-2020

Shashank is currently working as Senior Software Engineer at Gyansys Infotech with core expertise in Python, Generative AI, Data Engineering, and SQL. He completed his B.Tech. in Electrical and Electronics Engineering (EEE) in 2020 and has since focused on developing robust and scalable software systems. His college life was marked by active participation and leadership as a core member of both the Meta Academics Club and the Creative Club. This mix of technical depth and creative expression is further highlighted by his achievement of winning the Nukkad Natak (Street Play) competition in 2018 while representing his institution.



SUPRAVA PRUSTY
Computer Educator, RaspberryPi Foundation
B.Tech, (CSE), 2001-2005

Suprava Prusty is a Computer Educator at the Raspberry Pi Foundation, where she designs teacher-training programs to enhance digital literacy and integrate technology in classrooms. With a background in systems engineering at Redpine Signals and HP, she bridges industry experience with educational practices. Suprava is passionate about supporting underserved communities in India through tech-focused learning. Outside work, she enjoys singing and cherishes time with her two children, finding fulfillment in both her career and motherhood.

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