

EDITORIAL

Greetings from Silicon!

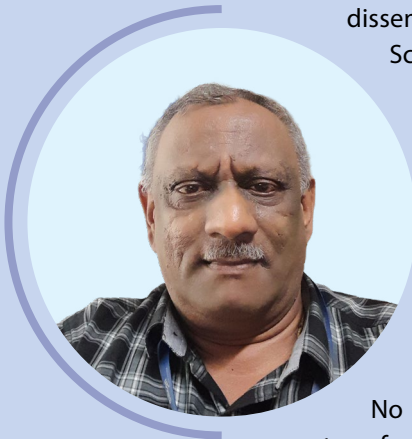
We are at the cusp of starting a new academic year, and a bit of retrospection is in order. As we reflect back on our university's journey over this past year, I am filled with optimism for what we are achieving together, both in academic pursuits and research endeavors.

Our commitment to the pursuit of excellence remains unwavering. Over the past year, our faculty members have continued to excel through high-quality research, industrial consultancy, scholarly publications and conference presentations together with grant-funded projects, showcasing our institutional capabilities; this is one of the prime objectives of a university where knowledge creation and dissemination takes place for the betterment of society. Our flagship Practice School program is thriving, with paid industry internships across a myriad of partner stations with an overwhelming majority of these internships convert to job placements. The overall campus placement stands at over seventy-five percent through drives by more than eighty companies.

All this is envisaged by the 5-year Silicon University Strategic Plan (SUSP), which covers five critical broad areas - Education, Research & Consultancy, People, Resources, and Engagement and Partnership. Targeted Key Performance Indicators are integrated with the SUSP, outlining deliverables over defined periods of time.

No discussion would be complete without acknowledging the recent transformative impact of Artificial Intelligence. AI is rapidly reshaping the way we learn, work, create, and innovate. Large language models such as ChatGPT, Claude, Gemini, Perplexity, Copilot, and DeepSeek are becoming integral components of the modern knowledge ecosystem. At Silicon, we are actively integrating AI-enabled tools and platforms into teaching-learning processes, instructional design, and student projects, ensuring that our graduates are prepared for an increasingly AI-driven world.

As we embark on another academic year, let us continue to challenge conventions, embrace innovation, and make a strong societal impact. Together, we can!



Dr. Jaideep Talukdar
Vice-Chancellor, Silicon University



MoU with Athena Security Solutions, India

Silicon University signed a Memorandum of Understanding (MoU) with Athena Security Solutions India Pvt. Ltd., Hyderabad, on 16 June 2026. The partnership establishes a long-term framework for collaborative research, development, prototyping, and deployment of advanced technologies for the protection of national critical infrastructure. It aims to accelerate the translation of research outcomes into practical, deployable solutions through close industry-academia collaboration. The MoU was signed by Prof. Pradipta Kumar Mohapatra, Registrar, Silicon University, and Mr. Vishnu Choudhary, Founder and Managing Director, Athena Security Solutions India Pvt. Ltd., marking the beginning of a strategic partnership.



NWET 2026

The Department of Electrical and Electronics Engineering (EEE) at Silicon University organized the 16th National Workshop on Emerging Technologies in Electrical Power Engineering (NWET-2026) on 3 and 4 April 2026. Themed on 'Transforming Energy Systems: Industry, Innovation, and Sustainability', the workshop featured a series of insightful presentations focusing on emerging technologies and the future of power systems. Prof. Ashok Kumar Tripathy, former Director General of the Central Power Research Institute (CPRI), was the Chief Guest of the workshop, while Mr. Prasanta Kumar Pattanaik, Director (Operations) at Odisha Power Transmission Corporation (OPTCL), joined as the Guest of Honour. The event concluded with a valedictory session, reinforcing the importance of innovation, and industry-academia collaboration.



M.Sc. (DHSM) Program Launch

Silicon University launched its M.Sc. in Digital Healthcare Systems and Management with an inaugural session featuring distinguished leaders from the healthcare and technology sectors. The event brought together industry experts and academicians to discuss the growing role of digital technologies in transforming healthcare delivery and management. Their insights highlighted emerging trends, evolving workforce requirements, and the need for interdisciplinary education to address future healthcare challenges. The program is designed to equip students with expertise at the intersection of healthcare, technology, and management, preparing them for leadership roles in the rapidly evolving digital health ecosystem.



CSHSSM 2026, a National Conference

The Department of Basic Sciences and Humanities (BSH) at Silicon University organized the National Conference on Science, Humanities, Social Sciences and Management (CSHSSM 2026) on 10 and 11 April 2026. The conference aimed to provide an interdisciplinary platform for researchers and academicians to present findings, exchange ideas, and foster collaboration across diverse fields. It also encouraged multidisciplinary research, academia-industry interaction, and discussions on emerging trends. The event received 41 research papers and 59 participants, including scholars from outside institutions. It featured one keynote lecture and five invited talks by eminent academicians, followed by technical sessions that facilitated knowledge sharing, discussion, and academic networking.



Techtronics 2026

The Silicon Innovation and Promotion Cell (SIPC) organized Techtronics 2026 on 11 April 2026 to promote innovation, practical learning, and teamwork. The event featured senior and junior prototype exhibitions and the newly introduced website development contest, Webtronics, attracting enthusiastic participation from schools and colleges. Teams from Trident Academy of Technology, Silicon University, Sambalpur University, ITER, DAV Chandrashekharapur, DAV Pokhariput, ODM Public School, and Venkateswar English Medium School were recognized for their outstanding performances. The event concluded with a felicitation ceremony including addresses by Dr. Ramaprasad Panda, Professor in EEE and Dr. Sudhansu Mohan Biswal, Associate Professor in EE.



RISE 2026

The Silicon Achievers' Club organized RISE 2026 (Research, Innovation, Science & Excellence) on 10 and 11 April 2026 to promote research, innovation, and academic excellence among students. The event encouraged participants to think critically, explore new ideas, and develop creative solutions to real-world challenges while fostering interdisciplinary learning and collaboration. It featured three events- Student Research Symposium, Clash of Perspectives, and Reverse Engineering, which showcased research presentations, debates, and analytical problem-solving skills. The event provided a platform for students from multiple institutions to demonstrate creativity and intellectual abilities, concluding with a strong emphasis on innovation and academic growth.



Expert talk on Rethinking Careers in AI

Silicon University organized an expert talk titled 'Rethinking Careers in the Age of AI' on 29 April 2026 as part of its Silver Jubilee Industry Talk Series. The session aimed to guide students in understanding evolving career opportunities in the era of artificial intelligence and digital transformation. Participants explored emerging domains such as cybersecurity and AI, gaining insights into industry trends, skill requirements, and future career pathways. The talk was delivered by Mr. Kamakhya Prasad Jena, Technology Head, Attack Surface Management Practice at TCS Cyber Security. He shared valuable industry perspectives on AI-driven transformation, cybersecurity challenges, and the importance of continuous learning for future professionals.



IEEE SUMMIT Visit by our Vice-Chancellor

Prof. Dr. Jaideep Talukdar, Vice-Chancellor of Silicon University, was invited to the Higher Education Summit titled "Reimagining Private Higher Education in India: Innovation, Sustainability and Competitiveness," held in New Delhi. The summit brought together academicians, education leaders, and policymakers to deliberate on the evolving landscape of private higher education in India. Discussions focused on fostering innovation, promoting sustainable growth, enhancing institutional competitiveness, and developing future-ready education models to address emerging challenges and opportunities in the higher education sector. The summit provided a platform to exchange ideas and share best practices.



World Environment Day 2026

The Eco-Social Club of Silicon University organized a plantation drive on the occasion of World Environment Day on 5 June 2026. The objective of the event was to spread awareness about the importance of plantation and environmental conservation to preserve Mother Earth. Faculty members from various departments and students from different years actively participated in the drive. The initiative encouraged collective responsibility towards nature and highlighted the significance of sustainable practices in maintaining ecological balance and promoting a greener future.



World DNA Day 2026

The JBS Haldane Centre for Molecular Medicine at Silicon University, organized two parallel three-day programs—Epigenetics & Disease Biology and Gene Therapy & Gene Editing—from 15–17 April 2026. The sessions provided insights into epigenetics, gene regulation, gene and cell therapy, CRISPR-Cas9 technology, and vector development through expert lectures and hands-on training. Prof. Biren Banerjee inaugurated the event. Keynote addresses were delivered by Prof. Tapas Kumar Kundu and Dr. Arkasubhra Ghosh, while expert sessions by Dr. Rahul Modak, Dr. Ruthrotha Selvi Bharathavikru, Dr. Barun Chatterjee, and Dr. Anuprita Ghosh enriched learning.



Industrial Visit to Odisha Power Transmission Corporation Limited (OPTCL), Chandaka

As part of the Summer Internship Program, the Industry Interface Cell of Silicon University organized an industrial visit to Odisha Power Transmission Corporation Limited (OPTCL), Chandaka, on 6 June 2026. The visit provided students with practical exposure to modern power transmission systems through a tour of a 220/132 kV grid substation. Students gained firsthand insights into the operation of power transformers, switchgears, circuit breakers, protection systems, busbars, and control rooms, while also learning about substation design, power transmission practices, operational reliability, and safety procedures followed in high-voltage installations.



Industrial Visit to NALCO Captive Power Plant (CPP), Angul

As part of the Summer Internship Program, the Industry Interface Cell of Silicon University organized an industrial visit to NALCO Captive Power Plant (CPP), Angul, on 13 June 2026. The visit provided students with firsthand exposure to power generation in a large industrial setting. They gained insights into the operation of thermal power generation units and observed key plant facilities, including boilers, turbines (from designated safe viewing areas), and auxiliary systems that ensure an uninterrupted power supply to NALCO's aluminium smelter complex. The visit was conducted under the guidance of experienced technical professionals, with strict adherence to established safety standards and operational protocols.

Note: For further details on any news item, visit <https://silicon.ac.in/news/>

Scopus/SCI indexed journals: **19**Conference Proceedings: **25**Book Chapters: **1**Patents: **9**

Design and Performance Investigation of a Dual p-GaN Recessed Gate High Electron Mobility Transistors for High Threshold Voltage Applications Journal Name- Semiconductors

Achieving a high positive threshold voltage ($V_{th} > 3V$) is critical for fail-safe operations in high-frequency switching and modern power electronics. While conventional enhancement-mode techniques like recessed gates or fluorine plasma treatments exist, they face long-term reliability issues and struggle to push V_{th} past 1 V. To bridge this gap, a novel normally-off HEMT architecture was developed and evaluated using TCAD simulations. The core innovation introduces a dual p-GaN configuration featuring a recessed p-GaN gate paired with a buried p-GaN region. Positioning these two regions strategically above and below the transistor channel optimizes the depletion of the twodimensional electron gas (2DEG), successfully driving the threshold voltage up to an exceptional 4.5 V. Performance metrics confirm that this structural modification does not compromise efficiency. Owing to sharp electrostatic modulation and tighter gate control, the device delivers a peak drain current (I_d) of 1 A/mm and a maximum transconductance (g_m) of 443 mS/mm. The architecture also records a remarkably low

ON-resistance (R_{on}) of 5.1 Ω .mm and a steep sub-threshold swing (SS) of 78 mV/dec. Furthermore, integrating a highly resistive buffer layer minimizes leakage currents and refines the ON-OFF current ratio, offering a superior V_{th} - I_d trade-off over existing designs. Moving forward, the next phase of this research will focus on developing a scalable mathematical model to accurately predict the transistor's behaviour across varying geometric dimensions. Future work will also involve a comprehensive reliability analysis to evaluate internal self-heating effects under high-power stress, alongside testing the device in circuit-level implementations to verify its performance in actual power converter topologies. From a commercial standpoint, a reliable 4.5 V threshold voltage directly satisfies the strict safety requirements of next-generation power grids. This makes the design highly relevant for immediate market integration into electric vehicle powertrains, aerospace systems, RADAR networks, and high-efficiency RF switching infrastructure.

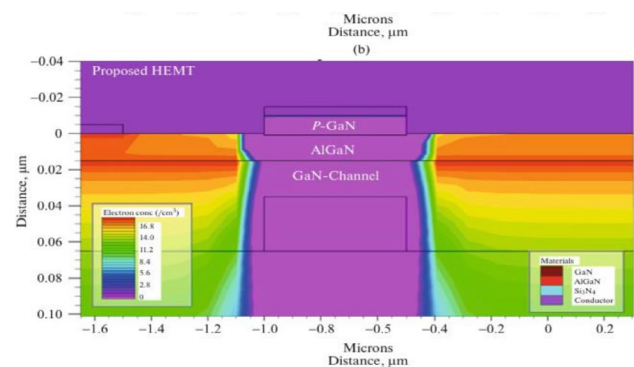
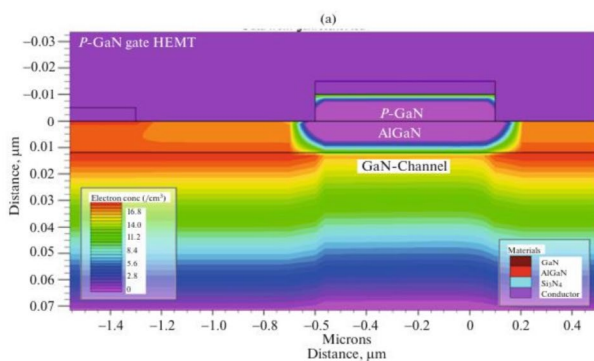


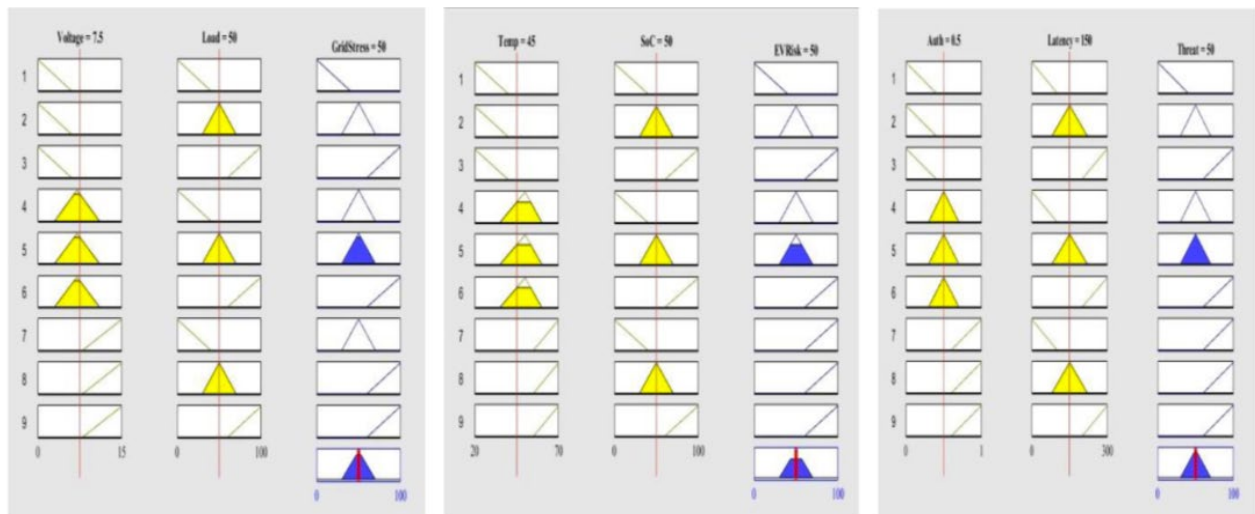
Figure: Electron concentration distribution over the channel, extracted from TCAD simulation of (a) p-GaN HEMT [25], and (b) proposed HEMT.

This research is being conducted by Mr. Joy Chowdhury, Assistant Professor, Department of Electronics and Communication Engineering.

A Semi-Supervised Approach of Cluster-Based Topic Modeling for Effective Tweet Hashtag Recommendation

The article focuses on the rapid expansion of electric vehicles (EVs) and the resulting challenges in managing charging infrastructure. With increasing EV adoption, power grids are facing significant stress due to uncoordinated charging demands, while cybersecurity risks such as data breaches and cyberattacks are becoming more prominent. The authors identify that traditional centralized systems are inadequate because of latency, scalability limitations, and vulnerability to attacks. To address these issues, the study proposes a novel Edge AI-based architecture designed to enhance the security, efficiency, and reliability of EV charging systems. Unlike conventional cloud-based systems, this approach shifts intelligence to the “edge” (i.e., charging stations and local nodes), enabling faster decision-making and real-time responsiveness. The architecture integrates distributed intelligence, fuzzy logic-based models, and real-time data analysis to monitor grid conditions, EV battery health, and potential cyber threats simultaneously. A key feature of the proposed system is its multi-dimensional decision-making capability, which combines grid stress analysis, cybersecurity monitoring, and load optimization into a unified framework. This allows the system to dynamically balance charging loads, preventing grid overloading while ensuring efficient energy distribution. The use of fuzzy logic enhances adaptability, enabling the system to respond effectively under uncertain and dynamic conditions. The research highlights several advantages of the Edge AI

approach. First, it significantly reduces response time compared to centralized systems, allowing quicker detection of anomalies and cyber threats. Second, it improves system scalability by distributing computational tasks across multiple nodes rather than relying on a single central server. Third, it enhances infrastructure resilience by enabling secure communication and coordination between EVs, charging stations, and grid systems. Simulation and analytical results demonstrate the effectiveness of the proposed architecture. The system successfully identifies abnormal charging behaviors, mitigates cybersecurity risks, and optimizes load distribution in real time. These improvements lead to better operational stability, increased efficiency, and enhanced protection against unauthorized access. In conclusion, the study presents a robust and scalable solution for modern EV charging challenges. By integrating Edge AI with advanced decision-making techniques, the proposed framework addresses critical issues related to grid stability, cybersecurity, and system efficiency. The research suggests that adopting decentralized, intelligent architectures is essential for the future of EV infrastructure, ensuring safe, reliable, and sustainable energy management as EV adoption continues to grow. Rule Viewer outputs of the three main fuzzy logic modules are as below: a) Rule Viewer of Grid Stability b) Rule Viewer of EV Health c) Rule Viewer of Cyber Threat.



a) Rule Viewer of Grid Stability b) Rule Viewer of EV Health c) Rule Viewer of Cyber Threat

This research is being conducted by Dr. Priyanka Kar, Associate Professor, Department of Electrical and Electronics Engineering.

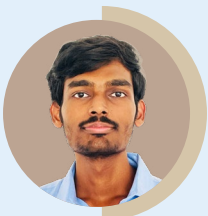
STUDENT ACHIEVERS



Birabhadra Sahoo from the B.Tech. CSE 2027 graduating batch gets placed at Infosys with a package of 16 LPA through HackWithInfy. His expertise lies in software engineering, Artificial Intelligence, Generative AI, and distributed backend systems.



Akansha Kumari from the B.Tech. CEN 2026 graduating batch secures a pre-placement offer from Micron Technology with a package of 14.5 LPA after successfully completing her 8th Semester PS program with Micron. Her current job role will include designing and developing efficient applications, collaborating with cross-functional teams, and leveraging AI to build intelligent, high-performance solutions.



M. Surya Kapil from the B.Tech. CSE 2027 graduating batch gets placed at Infosys with a package of 10 LPA through HackWithInfy. He has also secured a Summer Internship offer as a Software Engineer at PixelCompute for three months (June–August 2026) with a monthly stipend of 30K. Upon successful completion, he will continue under the Practice School (PS) Program with a monthly stipend of 42K.

Summer Internship (SI) Achievers

CreditNirvana
@60K



Ishaan Sinha
EEE, 2023-27

SN Space
Developers @55K



Sirajul Khan
CSE, 2025-29

Syber Inc
@35K



Ayushi Singh
CSE, 2023-27

PixelCompute
@30K



Md. Rizvi Hassan Ansari
CSE 2023-27

NEUOPTIC
@20K



Yash Raj
CSE, 2024-28

Athena Security
@15K



Pranav Kumar Jha
ECE, 2023-27

Practice School (PS) Selectees from 7th Semester B.Tech. Graduating Batch of 2027

DRDO



Kelli Yuvraj
CEN, 2023-27

Google Summer of Code@50K



Abhay Kumar Das
CST, 2023-27

DOGNOSIS@120K



Yuvraj Biswal
CEN, 2023-27

PLACEMENT HIGHLIGHTS

Despite the challenging hiring environment over the past few years, the placement performance at SiliconTech has remained strong and encouraging. In the last academic year, 75% of eligible students secured placements. The placement process for the current batch is still underway, and more than 73% of eligible B.Tech. students have already been placed. A significant highlight of this year's placements is that over 49% of the offers have been converted from internships under the Practice School (PS) Program. As part of this program, students undertook full-time internships during their final year, receiving monthly stipends ranging from ₹10K to ₹45K, which subsequently led to pre-placement offers and full-time employment. The highest package offered so far stands at ₹16.5 LPA by Tigerworld Technologies. Placement snapshot for eligible students, is given below.

B.Tech.*

73% Students placed
85 Companies participated
336 Internship offers
4.3 LPA Average package
14.5 LPA Highest package

MCA*

50% Students placed
34 Companies participated
7.09 LPA Highest package

M.Sc. (Data Science)*

40% Students placed
15 Companies participated
16.5 LPA Highest package

**Placement is continuing*

Practice School Highlights

2026 Graduating Batch

The Practice School (PS) program at SiliconTech has successfully completed five years of providing the students with valuable industry exposure and experiential learning opportunities. Despite evolving industry dynamics, the PS program has consistently delivered strong outcomes, reaffirming its relevance and effectiveness. Its increasing popularity among students reflects the growing importance of gaining practical industry experience during the final year of the B.Tech. program.

- **336 students** from the B.Tech. 2026 graduating batch successfully completed their PS Program.
- Students were offered monthly stipends ranging from **₹10K to ₹45K**. The highest stipend of ₹45K per month offered by **Naada Technologies**.
- **88 PS stations** partnered with SiliconTech this year, providing internship opportunities to **43.35%** of the graduating batch. The percentage of PS interns in core sector has increased to 29% this year compared to 16% in the previous year.
- The program also witnessed excellent placement outcomes, with nearly **70% of PS interns securing Pre-Placement Offers (PPOs)** till date from their host and other organizations.

2027 Graduating Batch

The Practice School (PS) program for the B.Tech. 2027 graduating batch has started on a positive note.

- Students have been selected to do their PS program during the 7th semester in prestigious companies including DOGNOSIS, Google Summer of Code and DRDO (Cyber).
- The highest stipend offered till date is ₹1.20 Lakhs per month by DOGNOSIS.

Summer Internship 2026

SiliconTech's Summer Internship Program 2026 commenced on 25 May 2026 and will continue till 4 July 2026.

- Spearheaded by II Cell, the program has introduced **34 industry-relevant and skill-enhancing courses** across a wide range of emerging technologies.
- This year, over **2356 students** enrolled in the Summer Internship Program which includes 76 participants from external institutions.
- Students from reputed institutes such as ABIT, ASBM, BJB College, Centurion University, DRIEMS University, GITA, IIIT Bhubaneswar, Institute of Mathematics, KL University, PMEC Berhampur, SOA University, Trident Academy, VIT, VSSUT, and XIM University actively participated in this program.
- The courses span across multiple domains, few of which are mentioned below:
 - Azure Masters and Data Engineering
 - Advancing with Rust
 - Machine Learning and Deep Learning for Industry
 - LLM Using Python & Python with FASTAPI
 - Digital VLSI Design from Verilog RTL and Verification to Synthesis
 - Industrial Automation (PLC, HMI and SCADA)
 - Full stack with Java Spring Boot with Angular MYSQL
 - Generative AI for Android: Firebase & Jetpack Compose
 - Electronic Sensor Design and Embedded Systems Integration for IoT Applications
 - Problem solving using DSA
 - SEO and Digital Marketing
 - Security Analysis Investing & Trading Labs
 - Cyber Security (SOC analyst & Ethical Hacking)
 - Applied Cybersecurity and Ethical Hacking
 - Specialized Training on Power System
- These programs were structured in accordance with prevailing industry trends and market requirements. The majority of the sessions were taken by industry experts, complemented by mentorship and academic guidance from SiliconTech faculty, ensuring a well-rounded and enriching learning experience for the participants
- Out of the 34 programs, 18 were delivered in offline mode, while 16 were conducted online, offering flexibility to participants.

The internship program received positive feedback from external participants for its expert-led instruction and exceptional training quality. Successfully coordinated by the II Cell, the initiative reinforces Silicon Tech's commitment to industry-focused education.



RTL-to-Chip: Practical Digital System Design and Case Study (Summer Internship 2026)

SUMMER INTERNSHIP 2026



Full stack with JAVA Spring Boot with Angular MySQL



Cyber Security (SOC analyst & Ethical Hacking)



Applied Artificial Intelligence & Industry AI Tools



Foundation Course on Core Industry



Digital System Verification using System Verilog



Electronic Sensor Design and Embedded
Systems Integration for IoT Applications



SparkUp Summit 2026

The Entrepreneurship Development Cell (EDCell) organized SparkUp Summit 2026, the annual entrepreneur's fest from 17 to 19 April 2026 to promote entrepreneurship, innovation, and startup culture among students and aspiring entrepreneurs. The three-day summit served as a flagship entrepreneurial event that brought together students, innovators, startup enthusiasts, academicians, and industry experts on a common platform to celebrate innovation, creativity, and entrepreneurship. The inaugural ceremony was graced by Prof. Jaideep Talukdar, Vice Chancellor of Silicon University, along with distinguished guests Mr. Binayak Acharya, Founder & CEO of ThinkZone, and Mr. Jagadish Sahu, Assistant Director, Micro, Small and Medium Enterprises – Development & Facilitation Office (MSME-DFO), Ministry of MSME, Government of India.

The first day of the summit featured the inaugural ceremony followed by competitions such as StockUp, Bidding War (Round 1), and Banner Expo. These activities witnessed enthusiastic participation from students and highlighted their creativity, business acumen, analytical thinking, and entrepreneurial mindset. The inauguration included ceremonial lamp lighting, keynote speeches, brochure



release, guest felicitation, and cultural performances, creating an energetic and inspiring atmosphere for all participants. The second and third days focused on innovation-driven activities and startup-oriented competitions. Events such as Idea Build Up, Bidding War Finale, and Startup Pitch Competition encouraged participants to present innovative business ideas and practical startup solutions. Students demonstrated leadership, teamwork, communication skills, strategic planning, and entrepreneurial thinking throughout the activities. The judges and mentors appreciated the participants for their innovative approaches and problem-solving abilities aimed at addressing real-world challenges.

The summit concluded with a grand valedictory ceremony and prize distribution program recognizing outstanding performances across various events. A total of 500 participants from different institutions actively participated in the summit, making the event a grand success. SparkUp Summit 2026 successfully fostered the spirit of innovation, leadership, collaboration, and entrepreneurship while inspiring young minds to explore startup culture and sustainable business solutions.



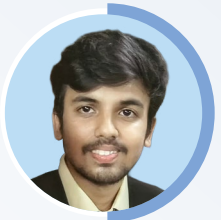
ALUMNI IN FOCUS



RANJAN KUMAR MOHAPATRA

Delivery Partner, TCS
B.Tech. (CSE) 2003–2007

Ranjan began his IT career as a Software Engineer at Satyam, where he spent over a decade and witnessed its transition to Tech Mahindra. Starting as a .NET developer, he later moved into the Data and Analytics domain, working with ETL and business intelligence technologies across development, support, and upgrade projects. Over the years, he progressed into roles such as Business Analyst, Technical Lead, Project Manager, and delivery leader. He later joined Infosys as a Service Delivery Manager, leading customer engagements across healthcare, insurance, and manufacturing sectors. Currently, he serves as a Delivery Partner at Tata Consultancy Services, leading the ServiceNow practice from Bhubaneswar and overseeing global delivery for the Technology Software & Services business group.



SAMBIT KUMAR NANDA

Software Engineer, Aptus Data Labs
B.Tech. (CSE) 2020-2024

Sambit works in software development with a focus on backend development and deployment, building reliable, scalable, and efficient systems. His work involves developing robust backend solutions, optimizing application performance, and ensuring seamless deployment of software applications. Driven by a strong curiosity for technology, he continuously explores diverse areas of computer science to broaden his technical expertise and stay updated with emerging trends. Passionate about learning and problem-solving, he enjoys expanding his knowledge beyond his core responsibilities. Outside work, he is an avid reader with a particular interest in fantasy and mythology.



PRASANT KUMAR PANIGRAHI

Associate Consultant, TCS
B. Tech. (EIE) 2004-2008

Prasant has built extensive expertise in PeopleSoft Application Maintenance and Support, with experience spanning production support, application development, system upgrades, governance, and AI-driven automation. Over the years, he has led strategic initiatives focused on improving operational efficiency, service quality, and process excellence while driving continuous improvement across enterprise applications. Passionate about leadership and innovation, he remains committed to leveraging emerging technologies to deliver business value. He is particularly interested in advancements in Artificial Intelligence and enjoys exploring new technologies, travelling, and spending quality time with his family.



RITESH KUMAR MOHAPATRO

ASP, CID, Crime Branch Odisha
B.Tech (ETC) 2005-2009

Ritesh Kumar Mohapatra serves as the Additional Superintendent of Police in the Cyber Crime Unit, CID, Crime Branch, Odisha. He leads cybercrime investigations and coordinates responses to online financial fraud and other cyber threats. With expertise in cybercrime investigation, digital forensics, and cyber intelligence, he has strengthened technology-driven policing and law enforcement capacity. He has played a key role in cyber awareness initiatives, training programs, and inter-agency coordination to enhance cybersecurity and public safety. He is passionate about leveraging emerging technologies, including Artificial Intelligence, to improve policing efficiency and proactive crime prevention.

Alumni Desk

Visit our Alumni Portal at <https://alumni.silicon.ac.in/> & register yourself

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